

Docket No.: P2001,0194

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : UWE MATTHIAS LEHR ET AL.  
Filed : CONCURRENTLY HEREWITH  
Title : INTEGRATED CIRCUIT HAVING ELECTRICAL CONNECTING  
ELEMENTS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,986,321 (Froehner), dated November 16, 1999;

U.S. Patent No. 6,172,896 B1 (Lee), dated January 9, 2001;

U.S. Patent No. 5,636,172 (Prall et al.), dated June 3, 1997;

European Patent Application EP 1 045 441 A2 (Chandrasekhar et al.), dated October 18, 2000;

International Search Report, dated March 25, 2003.

Respectfully submitted,



For Applicants **WERNER H. STEMER**  
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Date: September 15, 2003

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<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: P2001,0194 Appl. No.: <hr/> Applicant: UWE MATTHIAS LEHR ET AL. <hr/> Filing Date: September 15, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	5,986,321	11/16/99	Froehner			
	B	6,172,896 B1	1/9/01	Lee			
	C	5,636,172	6/3/97	Prall et al.			
	D						
	E						
	F						
	G						
	H						
	I						
<b>FOREIGN PATENT DOCUMENT</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
	J	1 045 441 A2	10/18/00	Europe			
	K						
	L						
	M						
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			